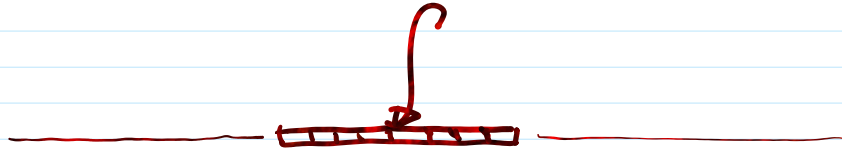


Administrivia.

- HW4 due May 9 (next Friday)



Last time in complexity land: $P = NP$?

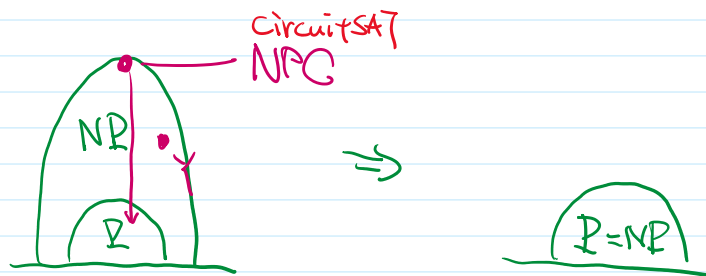
- An unnatural next Q:

Could it be that there's a hardest problem in NP?

NP-hard: If you can solve problem X fast^{in poly time},
then you can solve all problems in NP fast
(i.e. $NP = P$).

NP-complete: NP-hard + in NP.
circuit SAT

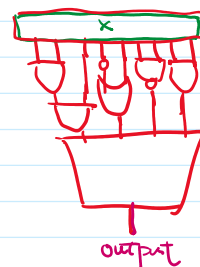
NP-complete : NP-hard + in NP.



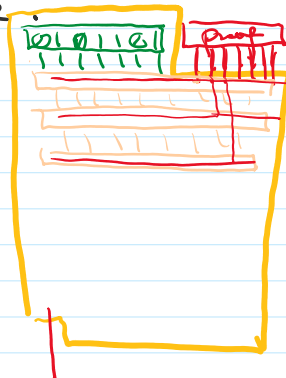
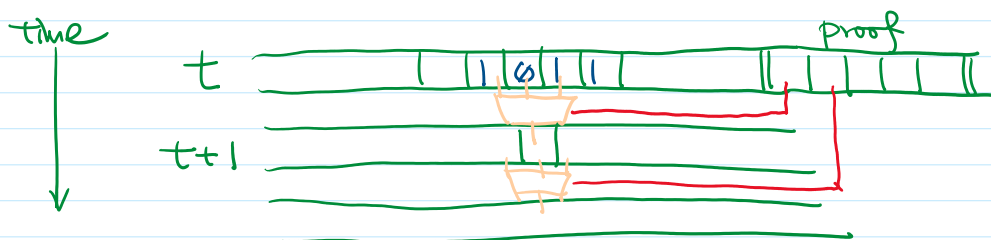
CIRCUIT SAT

input: A circuit consists of AND, OR, NOT gates, some input wires, one output wire

output: Is there an input to the circuit such that the output wire is 1?



Cook-Levin Thm. CIRCUIT SAT is NP-complete. NP-hard + NP.

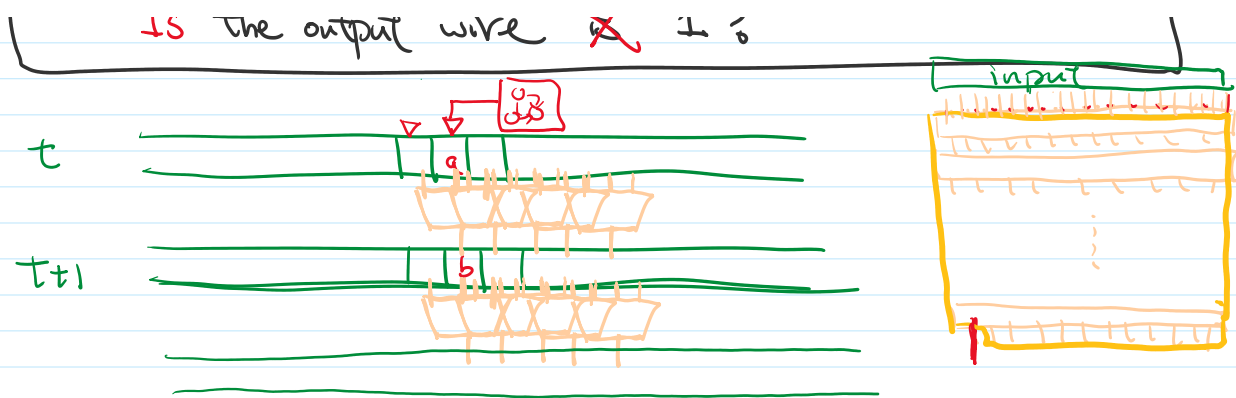


CIRCUIT VALUE

input: A circuit consists of AND, OR, NOT gates, some input wires, one output wire, input bits

output: ~~Is there an input to the circuit such that~~
Is the output wire ~~1~~ 1?





Thm. CIRCUITVALUE is in P. (actually P-complete)

Another triumph of the simplicity of TM model.

Question. Now what?

- more NPC problems?
- simpler SAT structures?



Reductions. subroutine/library

Problem A reduces to B $(A \leq B)$
 if solving B \Rightarrow solving A. _{poly.}

Q. Is A or B harder?

example. sorting \leq searching.

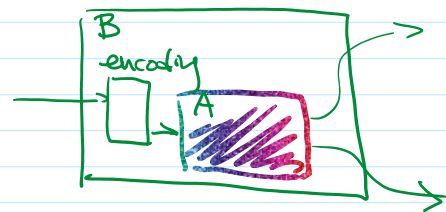
P finding triangle \leq finding biggest clique. NP

every NP problem \leq CIRCUITSAT.

every P problem \leq CIRCUITVALUE

To prove Problem A is NP-hard,
reduce known NP-hard problem to A

NP-hard $B \leq A$ thus A is NP-hard.



if you solve A fast, you also solve X fast;
but by def. of NP-hard, you solve all NP-problems fast
 \Rightarrow A is NP-hard as well.

The reduction has to run in poly. time.

example.

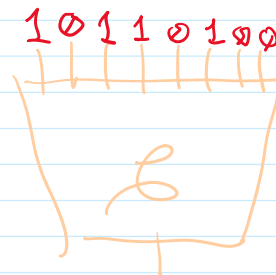
COMPUTESAT

input: a circuit \mathcal{C}

output: an input satisfying the circuit

NP-hard.

CIRCUITSAT \leq COMPUTESAT.



oracle reduction

example

CNFSAT

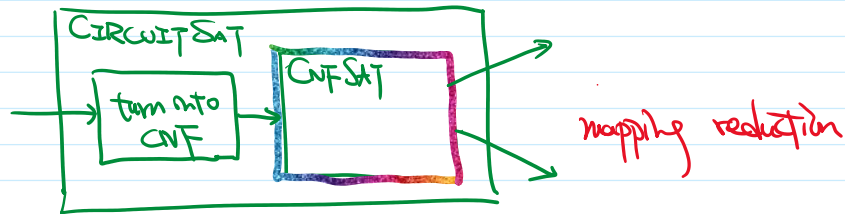
input: a CNF formula

output: is the formula satisfiable?

$(a \vee b \vee c \vee d) \wedge (b \vee \bar{c} \vee \bar{d}) \wedge (\bar{a} \vee c \vee d) \wedge (a \vee \bar{b})$

CIRCUITSAT \equiv CNFSAT :

turn a circuit into a CNF form.
 s.f. yes-inst. \Rightarrow yes-inst.
 no- no-



pf. (sketch) apply distrib. rule

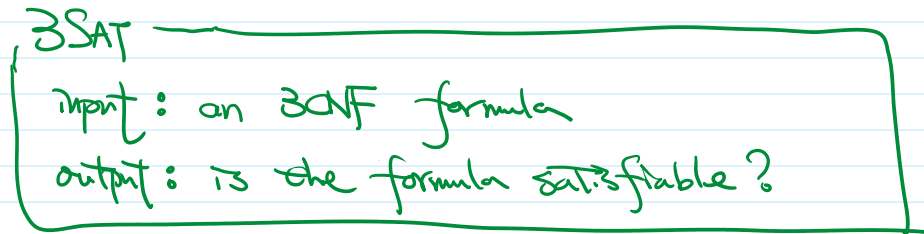
$$(x_1 \wedge \dots \wedge x_k) \vee (y_1 \wedge \dots \wedge y_l) \\ = (x_1 \vee y_1) \wedge \dots \wedge (x_k \vee y_l)$$

$$\& \quad \overline{(x_1 \wedge \dots \wedge x_k)} = \bar{x}_1 \vee \dots \vee \bar{x}_k$$

$$\overline{(x_1 \vee \dots \vee x_k)} = \bar{x}_1 \wedge \dots \wedge \bar{x}_k$$

the Boolean fun. computed does not change \square .

example.



SAT \equiv 3SAT :

$$(a \vee b \vee c \vee d) \wedge (b \vee \bar{c} \vee \bar{d}) \wedge (\bar{a} \vee c \vee d) \wedge (a \vee \bar{b})$$

$$(a \vee x_1) \wedge (\bar{x}_1 \vee b \vee x_2) \wedge (\bar{x}_2 \vee c \vee x_3) \wedge (\bar{x}_3 \vee d)$$

Thm. 3SAT is NP-hard



